

10-01-02

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Drawings

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TECHNOLOGY UNIT, LBN 2800

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OCT - 3 2002

In re the application:

Rayadurgam Ravikanth, et al

Serial No: 10/034,020

Filed: December 28, 2001

For: METHOD OF AND OPERATING ARCHITECTURAL ENHANCEMENT FOR
COMBINING OPTICAL PHOTONIC AND DATA PACKET-BASED
ELECTRICAL SWITCH FABRIC NETWORKS WITH A COMMON SOFTWARE
CONTROL PLANE WHILE PROVIDING INCREASED UTILIZATION OF
SUCH COMBINED NETWORKS.

The Honorable Commissioner
of Patents and Trademarks
Washington, DC 20231

Dear Sir:

With reference to the above, we enclose six sheets of formal drawings for filing
in the US Patent and Trademark Office.

Cordially,

RINES AND RINES

Robert H. Rines

Robert H. Rines, Reg. No. 15,932

Date: September 27, 2002

RINES and RINES, Reg. 16,118